Abbreviations

In the TI documents many abbreviations are introduced. To help clear up some of the confusion a partial list is provided in this appendix.

- **ATM** — Asynchronous Transfer Mode
- **CC** — EDMA Channel Controller
- **CODEC** — Coder/Decoder
- **CPU** — Central Processing Unit
- **DMA** — Direct Memory Access
- **DDR** — Double-Data Rate
- **DSP** — Digital Signal Processor
- **EDMA** — Enhanced Direct Memory Access
- **EMIF** — External Memory Interface
- **FIFO** — First-In First-Out
- **GMII** — Gigabit Media Independent Interface
- **HPI** — Host Port Interface
- **IDMA** — Internal DMA
- **L1D** — Level-1 Data Memory
- **L1P** — Level-1 Program Memory
- **LDW** — Load Word
- **LL2** — Local Level-2 Memory
- **MAC** — Media Access Control
- **MII** — Media Independent Interface
- **MPHY** — Multi-PHY
Appendix A • Abbreviations

PDMA — Pseudo-Direct Memory Access
PHY — Physical Interface
PLL — Phase-Locked Loop
RGMII — Reduced Gigabit Media Independent Interface
RMII — Reduced Media Independent Interface
Rx — Receive
S3MII — Source-Synchronous Serial Independent Interface
SCR — Switched Central Resource
SDRAM — Synchronous Dynamic Random Access Memory
SL2 — Shared Level-2 Memory
SPHY — Single-PHY
SRAM — Static Random Access Memory
SRIO — Serial RapidIO
STW — Store Word
TC — EDMA Transfer Controller
TR — Transfer Request
TSIP — Telephony Serial Interface Port
Tx — Transmit
UTOPIA — Universal Test and Operations PHY Interface for ATM